|  |  |
| --- | --- |
| TNC Testing Form (REV1) | |
| Leaf on the Tree | Packet Construction |
| Device Under Test (Testing Tree Number): | 2.2.2.1 |
| Date: | 11/1/20 |
| Person(s) Conducting Experiment: | Kobe Keopraseuth |
| Signature: |  |
| Experiment Purpose: | The purpose of this experiment is to validate that the microcontroller will output the AX.25 (excluding the flags) in correct order. |
| Experiment Procedure: | I will display how the AX.25 packet will be sent to the radio, using a serial monitor. |
| Equipment Settings / Software Settings (w Revision): | We will be using Rizwan’s software to send a KISS packet over UART, and we will be using visual studio’s serial monitor to display the AX.25 packet’s bit sequence. |
| Testing Diagram / Picture: |  |
| Data Points: | A picture containing text  Description automatically generated  **Bitnary Bit stream of packet**    **Binary Bitstream of how AX.25 packet will be sent to radio** |
| Pass / Fail: | Pass |
| Interpreted Notes: | As shown on the serial monitor, the AX.25’s bits are in the correct order. FCS field is sent MSB first and other fields are sent LSB first. After 5 contiguous ones then a bit stuffed zero is added after. |
| Recommendations for Modifications: | None |